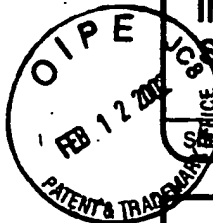


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		Filing Date	October 12, 2001
		First Named Inventor	Stephen O'Connor
		Group Art Unit	Unknown
		Examiner Name	Unknown
		Attorney Docket Number	PA1971US
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mj		10 Gigabit Ethernet Alliance [online]. "10 Gigabit Ethernet Technology Overview White Paper", May, 2001 Revision 1.0. Retrieved from Internet: <URL: http://www.10gea.org >.	
mj		AGERE SYSTEMS, "10 Gigabit Ethernet PCS Intellectual Property Cores (Preliminary Data Sheet)", July 2001. Retrieved from <URL: www.agere.com >.	
mj		DUGAN, et al., "64b/66b Coding Update" [Presentation Materials], IEEE 802.3ae, Albuquerque, NM, March 6, 2000.	
mj		MCCARLEY [Initiator of discussion], 'Re: 64b/66b gearbox in FPGA.' (Threaded Discussion on the Comp.arch.fpga vsnet newsgroup) [online], Jan./Feb., 2001. Retrieved from Internet: <URL: www.fpga-faq.com/archives/28975.html > and similar pages [retrieved on 22 June 2001].	

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